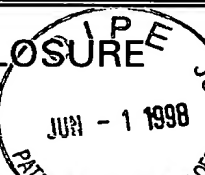


INFORMATION DISCLOSURE CITATION

PTO-1449

ATTORNEY'S DKT NO.
015290-238APPLICATION NO.
09/002,007APPLICANT
Jeff HUNG et al.FILING DATE
December 31, 1998GROUP
1763

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
LV	5,126,289	6/30/92	Ziger <i>D. 28</i>	437	231	
LV	5,308,742	5/3/94	Ta <i>D. 28</i>	430	713	
LV	5,655,110	8/5/97	Krivokapic et al.	395	500	

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

LV	Silicon Processing for the VLSI Era, Stanley Wolf et al., 1987, Process Technology, Lattice Press, Sunset Beach, CA, Vol 1, pp 438-441

EXAMINER
LAN VINHDATE CONSIDERED
4/15/99

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION				ATTORNEY'S DKT NO. 015290-238		APPLICATION NO. 09/002,007	
				APPLICANT Jeff HUNG et al.			
				FILING DATE December 31, 1997		GROUP 1763	
PTO-1449							
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
LV	5,126,289	6/30/92	Ziger	437	231		
↓	5,256,564	10/26/93	Narita	437	195		
	5,269,879	12/14/93	Rhoades et al.	156	643		
	5,308,742	5/3/94	Ta	430	3313		
	5,437,961	8/1/95	Yano et al.	430	3016		
	5,445,710	8/29/95	Hori et al.	156	6321		
	5,486,493	1/23/96	Jeng	437	195		
↓	5,508,881	4/16/96	Stevens	361	305		
	5,554,560	9/10/96	Hsue et al.	437	69		
	LV	5,591,480	1/7/97	Weisman et al.	216	13	
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes No	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER	LAN VINH			DATE CONSIDERED 4/15/99			

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